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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
10/612,963	07/07/2003	Shin-Tai Lo	2450-0494P	4750	
2292	7590 08/20/2004		EXAMINER		
BIRCH STEWART KOLASCH & BIRCH PO BOX 747			TRAN, T	TRAN, THUY V	
FALLS CHURCH, VA 22040-0747			ART UNIT	PAPER NUMBER	
			2821		

DATE MAILED: 08/20/2004

Please find below and/or attached an Office communication concerning this application or proceeding.

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	Application No.	Applicant(s)			
	10/612,963	LO ET AL.			
Office Action Summary	Examiner	Art Unit			
	Thuy V. Tran	2821			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply					
A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 03 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely. - If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication. - Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).					
Status					
1) Responsive to communication(s) filed on <u>07 July 2003</u> .					
2a) ☐ This action is FINAL . 2b) ☒ This	This action is FINAL . 2b)⊠ This action is non-final.				
, <u> </u>	Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.				
Disposition of Claims					
4) Claim(s) 1-5 is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration. 5) Claim(s) is/are allowed. 6) Claim(s) 1-5 is/are rejected. 7) Claim(s) is/are objected to. 8) Claim(s) are subject to restriction and/or election requirement.					
Application Papers					
 9) The specification is objected to by the Examiner. 10) The drawing(s) filed on <u>07 July 2003</u> is/are: a) accepted or b) objected to by the Examiner. Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a). Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d). 11) The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152. 					
Priority under 35 U.S.C. § 119					
 12) Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f). a) All b) Some * c) None of: Certified copies of the priority documents have been received. Certified copies of the priority documents have been received in Application No Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)). * See the attached detailed Office action for a list of the certified copies not received. 					
Attachment(s) 1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948) 3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date 7/7/03.	4) Interview Summary Paper No(s)/Mail Da 5) Notice of Informal P 6) Other:				

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DETAILED ACTION

This is a response to the Applicants' filing on July 7th, 2003. In virtue of this filing, claims 1-5 are currently presented in the instant application.

Inventorship

1. This application currently names joint inventors. In considering patentability of the claims under 35 U.S.C. 103(a), the examiner presumes that the subject matter of the various claims was commonly owned at the time any inventions covered therein were made absent any evidence to the contrary. Applicant is advised of the obligation under 37 CFR 1.56 to point out the inventor and invention dates of each claim that was not commonly owned at the time a later invention was made in order for the examiner to consider the applicability of 35 U.S.C. 103(c) and potential 35 U.S.C. 102(e), (f) or (g) prior art under 35 U.S.C. 103(a).

Abstract Objection

2. The abstract of the disclosure is objected to because of the following informality:

Line 3 (of page 11), "consists of" should be changed to --comprises--.

Correction is required. See MPEP § 608.01(b).

Claim Objections/ Minor Informalities

3. Claims 1 and 5 are objected to because of the following informalities:

Claim 1, line 2, "consists of" should be changed to --comprises--;

Claim 1, line 12 (or line 13 of page 9), "unit" should be changed to --end--; and

Claim 5, line 2, "consists of" should be changed to --comprises--.

Appropriate correction is required.

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Claim Rejections - 35 USC § 102

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

The changes made to 35 U.S.C. 102(e) by the American Inventors Protection Act of 1999 (AIPA) and the Intellectual Property and High Technology Technical Amendments Act of 2002 do not apply when the reference is a U.S. patent resulting directly or indirectly from an international application filed before November 29, 2000. Therefore, the prior art date of the reference is determined under 35 U.S.C. 102(e) prior to the amendment by the AIPA (pre-AIPA 35 U.S.C. 102(e)).

5. Claims 1-5 are rejected under 35 U.S.C. 102(e) as being anticipated by Bae et al. (U.S. Patent No. 6,580,408).

With respect to claim 1, Bae et al. discloses, in Fig. 1, an apparatus for generating uniform images of an active matrix organic light emitting diode display device which comprises a plurality of pixel devices; each of the pixel devices comprises (1) a switch unit [M1] having two input ends and an output end; the two input ends connect respectively to a data line [D1] and a scan line [G1], (2) a storage unit [Csto] having one end connecting to a supply line [Vdd] and another end connecting to the output end of the switch unit [M1], (3) a driver unit [M2] having two input ends and an output end; one input end connects to the supply line [Vdd] and another input end connects to the output end of the switch unit [M1], and (4) an OLED [EL] having an anode and a cathode; the anode is connected to the output end of the driver unit [M1] and the cathode is connected to a positive power supply [Vss].

With respect to claim 2, Fig. 1 of Bae et al. shows that the switch unit [M1] is a thin film transistor.

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With respect to claim 3, Fig. 1 of Bae et al. shows that the driver unit [M2] is a thin film transistor.

With respect to claim 4, Fig. 1 of Bae et al. shows that the storage unit [Csto] includes a capacitor.

With respect to claim 5, Bae et al. discloses, in Fig. 1, a method for generating uniform images of an active matrix organic light emitting diode display device which comprises a plurality of pixel devices; each of the pixel devices comprises a driver unit [M2] to drive an OLED [EL] to display; the method comprises the steps of (1) connecting a cathode of the OLED to a positive power supply [Vss] to provide a voltage to increase the electric potential of the OLED [EL], (2) reducing the voltage difference of the source electrode and the drain electrode of the driver unit [M2] during operation, and (3) keeping the voltage difference of the source electrode and the gate electrode unchanged so that output current fluctuations of the driver unit [M2] decrease while the driver unit [M2] is ON when the threshold voltages are different due to characteristic variations of the driver unit [M2] (see col. 1, lines 31-64).

Citation of relevant prior art

6. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Prior art Seo et al. (Pub. No.: US 2003/0227253 A1) discloses a display device and method for manufacturing thereof.

Prior art Yamazaki et al. (Pub. No.: US 2003/0201727 A1) discloses a light emitting display device.

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Prior art Tanada (Pub. No.: US 2002/0047559 A1) discloses a self-light emitting display device.

Prior art Lo et al. (U.S. Patent No. 6,777,886) discloses a display device and a digital driving method.

Inquiry

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Thuy V. Tran whose telephone number is (571) 272-1828. The examiner can normally be reached on M-F (8:00 AM -5:00 PM).

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Don Wong can be reached on (571) 272-1834. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Thuy V. Tran Examiner Art Unit 2821

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08/18/2004